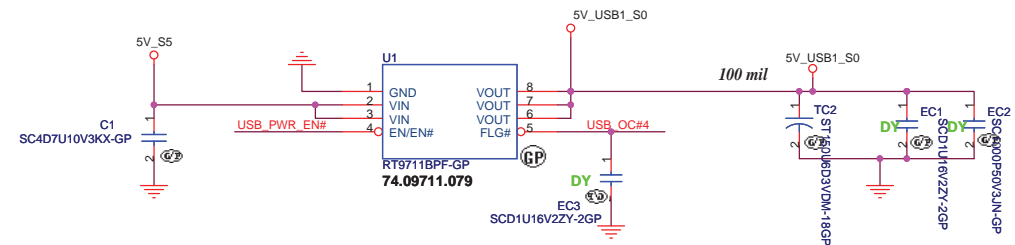
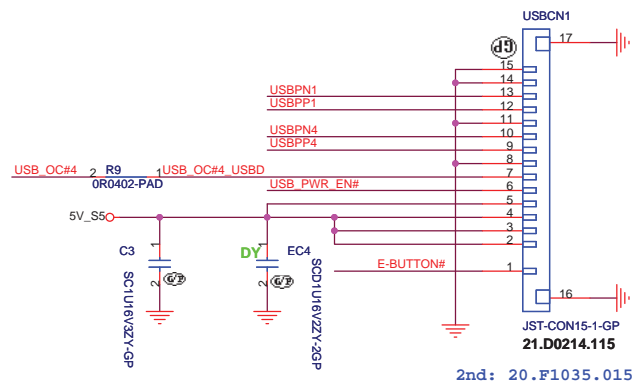
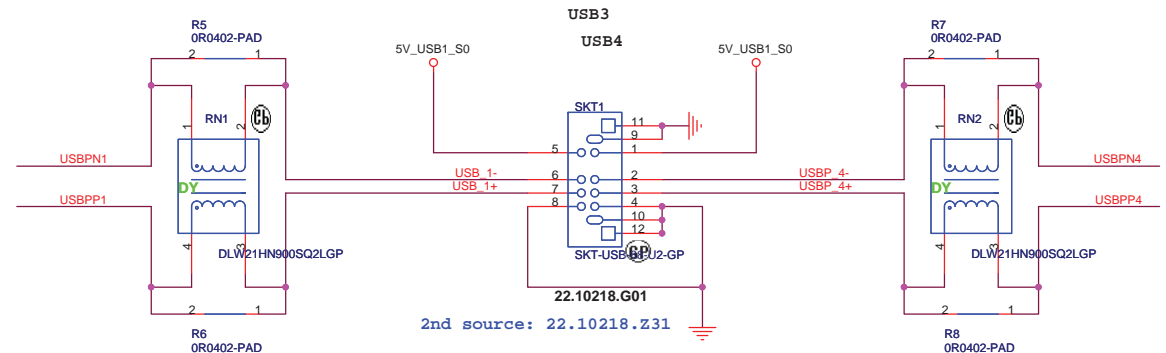
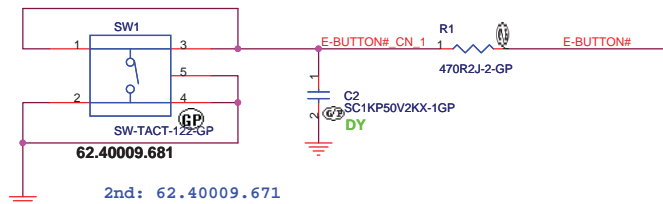


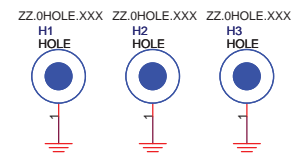
Cathedral Peak USB Board

Project code: 91.4J501.001
PCB P/N : 48.4J502.0SB
REVISION : 07951-SB

E Power Button



E-BUTTON#	1	TP1	TPAD34
5V_S5	1	TP2	TPAD34
USB PWR EN#	1	TP3	TPAD34
USB OC#4 USB	1	TP4	TPAD34
USBPP4	1	TP5	TPAD34
USBPN4	1	TP6	TPAD34
USBPP1	1	TP7	TPAD34
USBPN1	1	TP8	TPAD34



<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title USB Board		
Size A3	Document Number Cathedral Peak	Rev SB
Date: Thursday, March 06, 2008	Sheet 1 of 1	